

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Low Thermal Resistance

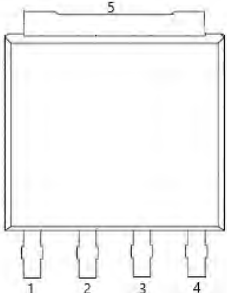
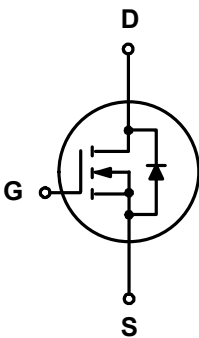
1.2 Applications

- ☒ Motor drivers
- ☒ DC - DC Converter

1.3 Quick reference

- ☒ $BV \geq 120\text{ V}$
- ☒ $R_{DS(ON)} \leq 5.0\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- ☒ $P_{tot} \leq 71\text{ W}$
- ☒ $R_{DS(ON)} \leq 7.5\text{ m}\Omega @ V_{GS} = 6\text{ V}$
- ☒ $I_D \leq 73\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 Top View LFPAK	
4	Gate		
5	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	-	120	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 20	V
I _D	Drain Current	T _C = 25 °C, V _{GS} = 10 V	-	73	A
		T _C = 100 °C, V _{GS} = 10 V	-	51	A
I _{DM} **	Pulsed Source Current	T _C = 25 °C, V _{GS} = 10 V	-	292	A
P _{tot}	Total Power Dissipation	T _C = 25 °C	-	71	W
T _{stg}	Storage Temperature		- 55	175	°C
T _J	Junction Temperature		-	175	°C
I _S	Diode Forward Current	T _C = 25 °C	-	73	A
EAS	Single Pulsed Avalanche Energy	V _{DD} = 50 V , L = 1.0mH	-	925	mJ
R _{θJA} *	Thermal Resistance- Junction to Ambient		-	58	°C / W
R _{θJC}	Thermal Resistance- Junction to Case		-	2.1	

Notes :

- * Surface Mounted on 1 in² pad area, t ≤ 10 sec
- ** Pulse width ≤ 300 μs, duty cycle ≤ 2 %
- *** Limited by bonding wire

4. Marking Information

Product Name	Marking
UK045N12LFH	045N12H YWWXXX AAAAAA AAAAAA: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
UK045N12LFH	LFPK5*6			5000	

6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _{DS} = 250 μA	120	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	2	-	4	V
I _{DSS}	Drain Leakage Current	V _{DS} = 96 V, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = 0 V, V _{GS} = ± 20 V	-	-	±100	nA
R _{DS(ON)} ^a	On-State Resistance	V _{GS} = 10 V, I _{DS} = 30 A	-	4.3	5.0	mΩ
		V _{GS} = 6 V, I _{DS} = 20 A	-	6.8	7.5	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 30 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{DS} = 30 A, V _{GS} = 0 V dI _{SD} /dt = 100 A/μs	-	97	-	nS
Q _{rr}	Reverse Recovery Charge		-	323	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 60 V Frequency = 1 MHz	-	4479	-	pF
C _{oss}	Output Capacitance		-	629	-	
C _{rss}	Reverse Transfer Capacitance		-	30	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 60 V, V _{GEN} = 10 V, R _G = 3.9 Ω, R _L = 2 Ω, I _{DS} = 30 A	-	18	-	nS
t _r	Turn-on Rise Time		-	35	-	
t _{d(off)}	Turn-off Delay Time		-	40	-	
t _f	Turn-off Fall Time		-	26	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{DS} = 60 V, V _{GS} = 10 V, I _{DS} = 30 A	-	73	-	nC
Q _{gs}	Gate-Source Charge		-	26	-	
Q _{gd}	Gate-Drain Charge		-	18	-	

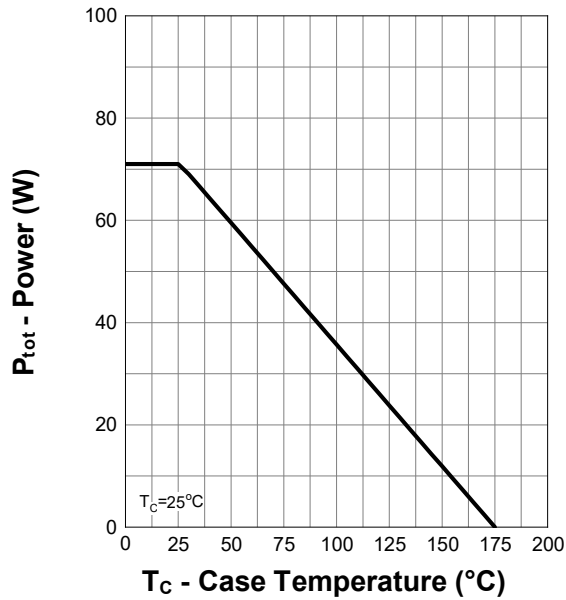
Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2%

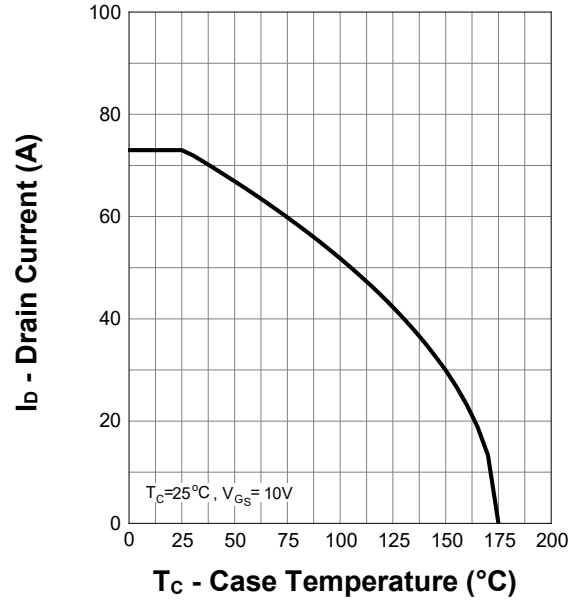
b : Guaranteed by design, not subject to production testing

7. Typical Characteristics

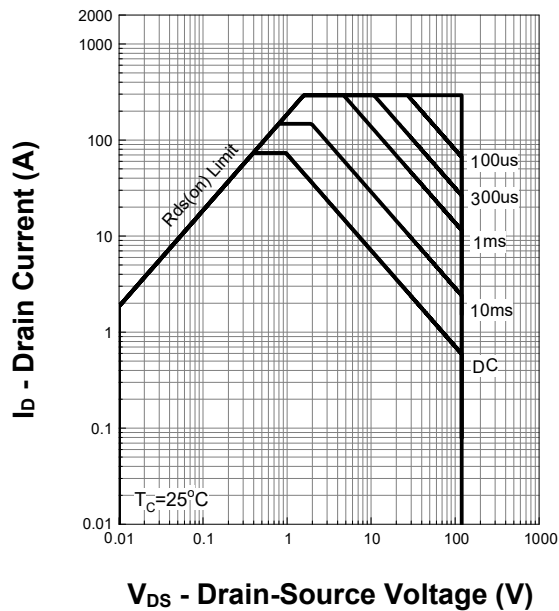
Power Capability



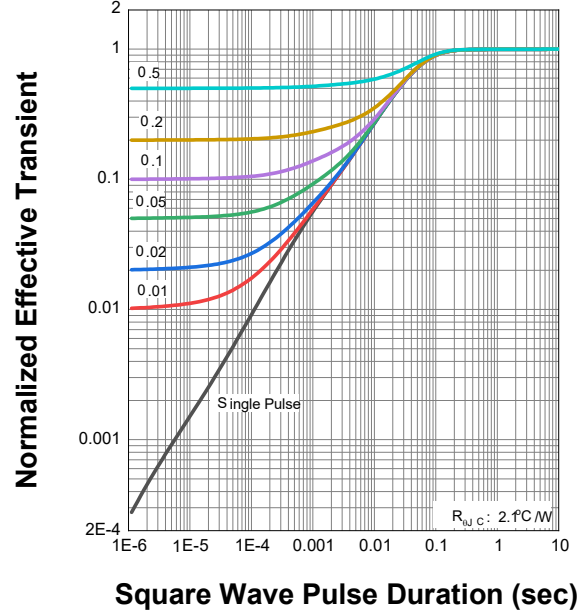
Current Capability



Safe Operating Area

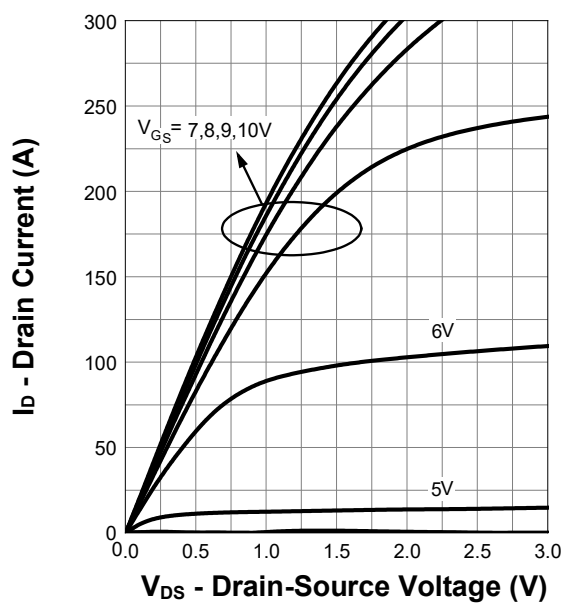


Transient Thermal Impedance

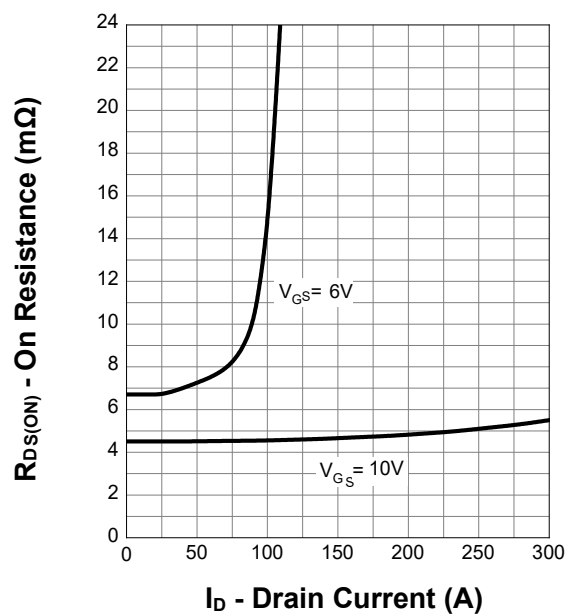


7. Typical Characteristics (cont.)

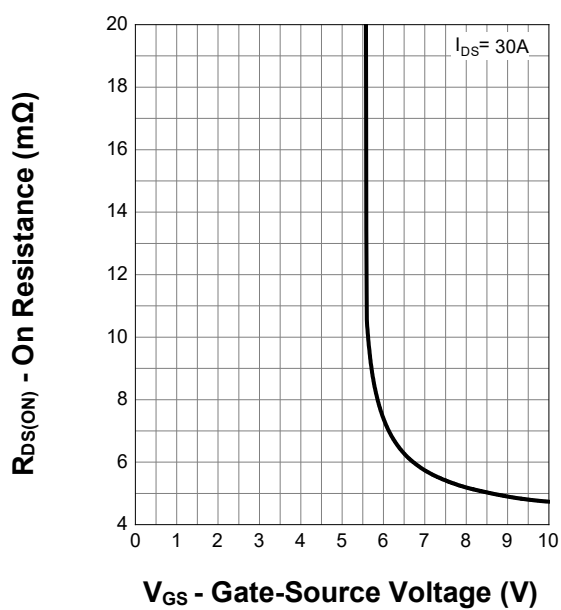
Output Characteristics



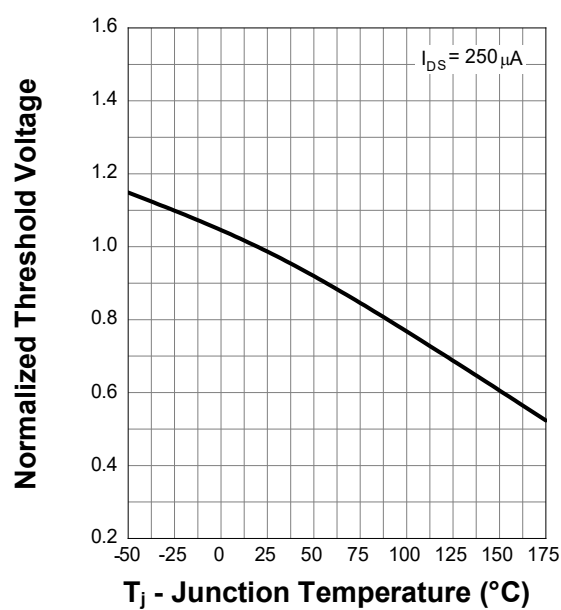
On Resistance



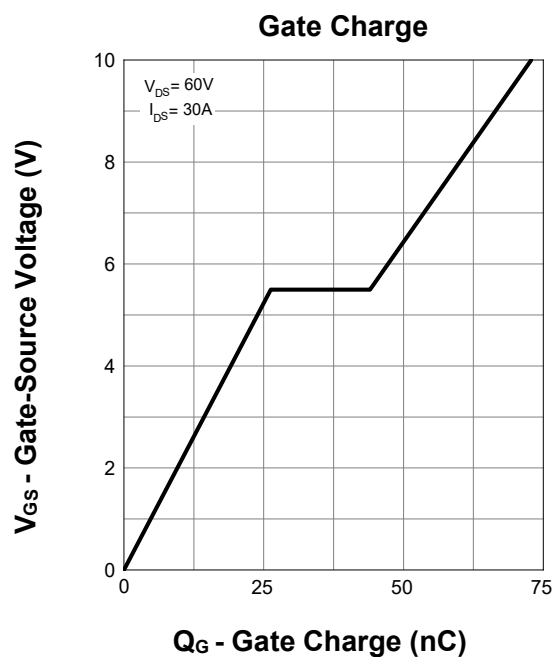
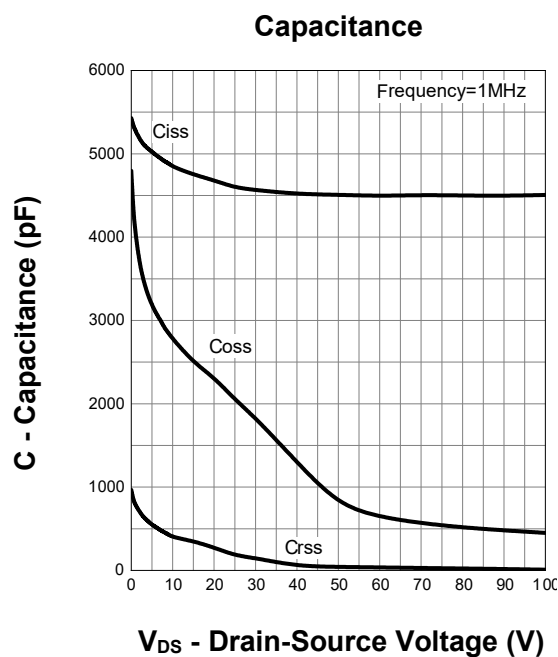
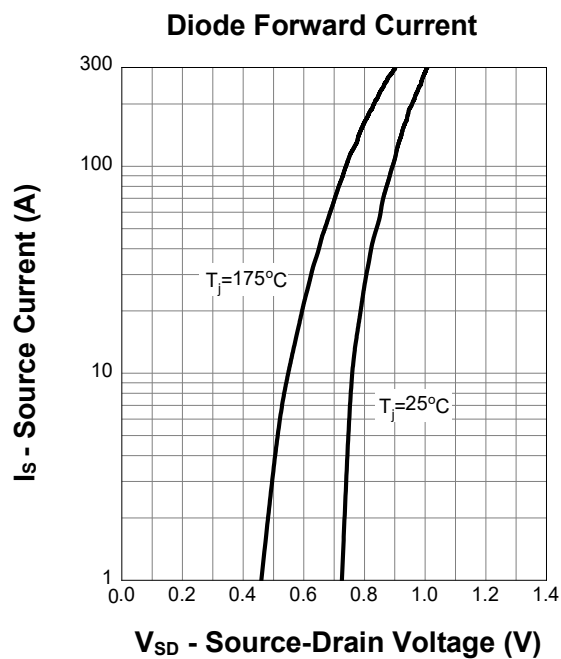
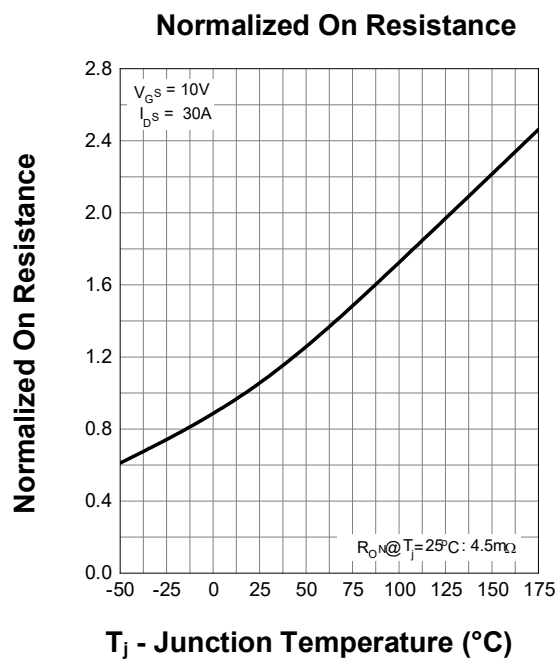
Transfer Characteristics



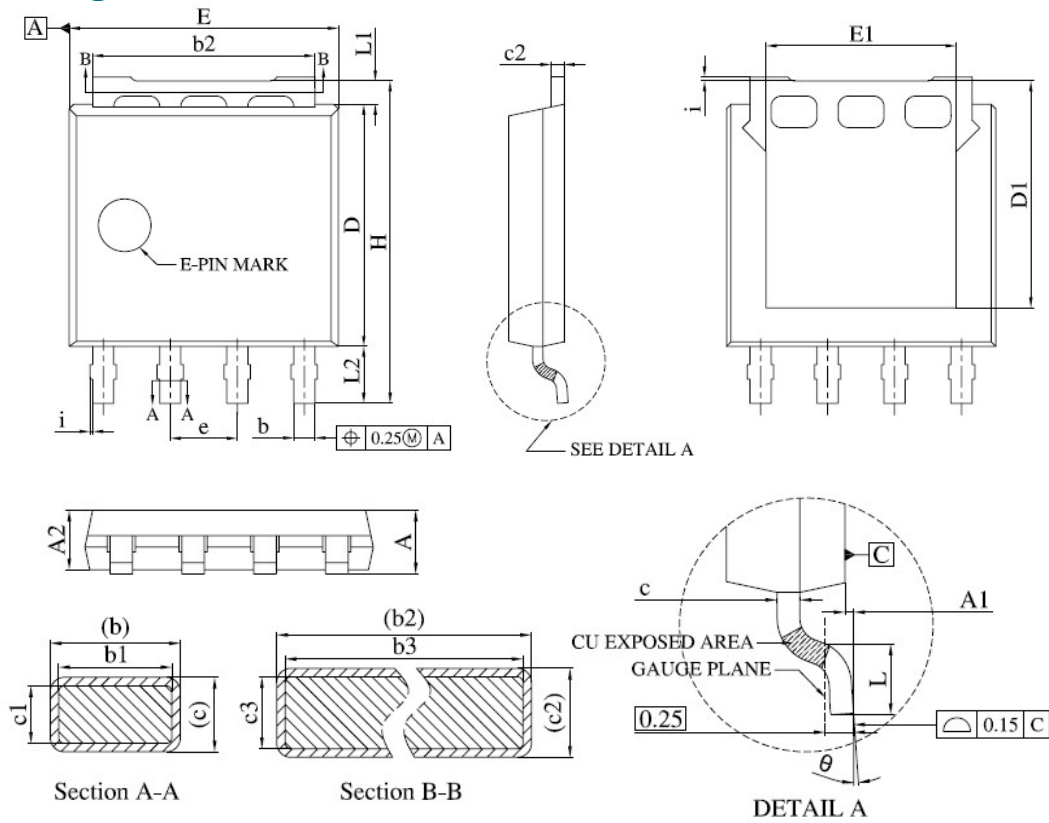
Normalized Threshold Voltage



7. Typical Characteristics (cont.)



8. Package Dimensions LFPAK5*6 Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.00	1.30
A1	0.00	0.15
A2	0.98	1.12
b	0.35	0.50
b1	0.32	0.46
b2	4.02	4.41
b3	4.00	4.37
c	0.19	0.25
c1	0.17	0.23
c2	0.24	0.30
c3	0.22	0.28
D	4.45	4.70
D1	-	4.45
E	4.95	5.30
E1	3.50	3.70
e	1.27 BSC.	

Symbol	Dimensions In Millimeters	
	MIN.	MAX.
H	5.95	6.25
i	-	0.25
L	0.40	0.85
L1	0.27	0.57
L2	0.80	1.30
θ	0°	8°