

N-Channel Enhancement Mode MOSFET

1. Product Information

1.1 Features

- ☒ Surface-mounted package
- ☒ Low Thermal Resistance

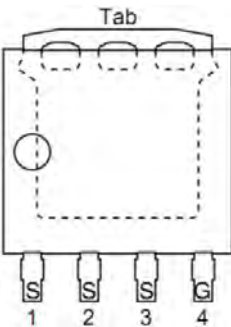
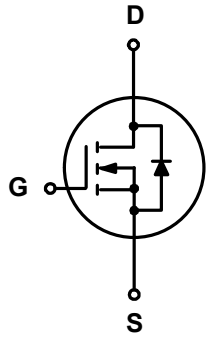
1.2 Applications

- ☒ Motor drivers
- ☒ DC - DC Converter

1.3 Quick reference

- ☒ $BV \geq 40\text{ V}$
- ☒ $R_{DS(ON)} \leq 0.55\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- ☒ $P_{tot} \leq 375\text{ W}$
- ☒ $R_{DS(ON)} \leq 1.65\text{ m}\Omega @ V_{GS} = 6\text{ V}$
- ☒ $I_D \leq 325\text{ A}$

2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	 Top View LFPAK5*6	
4	Gate		
Tab	Drain		

3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V _{DS}	Drain-Source Voltage	T _C = 25 °C	-	40	V
V _{GS}	Gate-Source Voltage	T _C = 25 °C	-	± 20	V
I _D ^{*,***}	Drain Current	T _C = 25 °C, V _{GS} = 10 V	-	325	A
		T _C = 100 °C, V _{GS} = 10 V	-	325	A
I _{DM} ^{*,**}	Pulsed Source Current	T _C = 25 °C, V _{GS} = 10 V	-	1300	A
P _{tot} [*]	Total Power Dissipation	T _C = 25 °C	-	375	W
T _{stg}	Storage Temperature		- 55	175	°C
T _J	Junction Temperature		-	175	°C
I _S	Diode Forward Current	T _C = 25 °C	-	325	A
E _{AS} [*]	Single Pulsed Avalanche Energy	V _{DD} = 40 V, L = 1 mH	-	1152	mJ
R _{θJA} [*]	Thermal Resistance- Junction to Ambient		42	-	°C / W
R _{θJC} [*]	Thermal Resistance- Junction to Case		0.4	-	

Notes :

* Surface Mounted on 1 in² pad area, t ≤ 10 sec

** Pulse width ≤ 300 μs, duty cycle ≤ 1 %

*** Limited by bonding wire

4. Marking Information

Product Name	Marking
UK005N04LFH	005N04H YWW01 AAAAAA YWW01: Date Code

5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
UK005N04LFH	LFPK5*6			5000	

6. Electrical Characteristics (T_A = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	40	-	-	V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _{DS} = 250 μA	2	-	4	V
I _{DSS}	Zero Gate Voltage Source Current	V _{DS} = 32, V _{GS} = 0 V	-	-	1	μA
I _{GSS}	Gate Leakage Current	V _{GS} = ± 20 V, V _{DS} = 0 V	-	-	± 100	nA
R _{DS(ON)} ^a	Drain-Source On-State Resistance	V _{GS} = 10 V, I _D = 50 A	-	0.50	0.55	mΩ
		V _{GS} = 6 V, I _D = 30 A	-	1.42	1.65	
Diode Characteristics						
V _{SD} ^a	Diode Forward Voltage	I _{SD} = 50 A, V _{GS} = 0 V	-	-	1.3	V
t _{rr}	Reverse Recovery Time	I _{SD} = 45 A, dI _{SD} /dt = 100 A/μs	-	76	-	nS
Q _{rr}	Reverse Recovery Charge		-	54	-	nC
Dynamic Characteristics ^b						
C _{iss}	Input Capacitance	V _{GS} = 0 V, V _{DS} = 20 V Frequency = 1 MHz	-	7204	-	pF
C _{oss}	Output Capacitance		-	2944	-	
C _{rss}	Reverse Transfer Capacitance		-	147	-	
t _{d(on)}	Turn-on Delay Time	V _{DS} = 20 V, V _{GEN} = 10 V, R _G = 3.9 Ω, R _L = 0.4 Ω, I _{DS} = 50 A	-	26	-	nS
t _r	Turn-on Rise Time		-	105	-	
t _{d(off)}	Turn-off Delay Time		-	81	-	
t _f	Turn-off Fall Time		-	72	-	
Gate Charge Characteristics ^b						
Q _g	Total Gate Charge	V _{GS} = 10 V, V _{DS} = 20 V, I _{DS} = 50 A	-	121	-	nC
Q _{gs}	Gate-Source Charge		-	40	-	
Q _{gd}	Gate-Drain Charge		-	31	-	

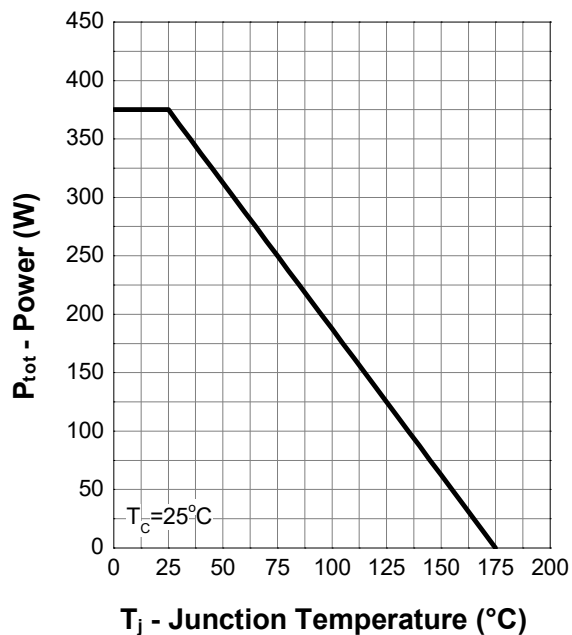
Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

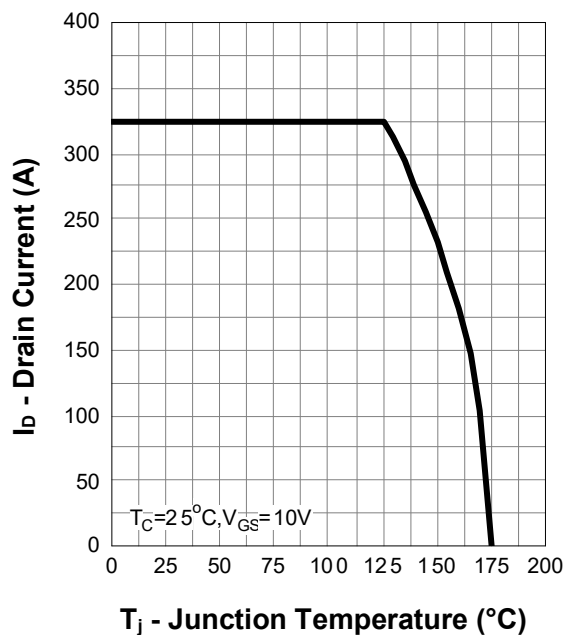
b : Guaranteed by design, not subject to production testing

7. Typical Characteristics (Cont.)

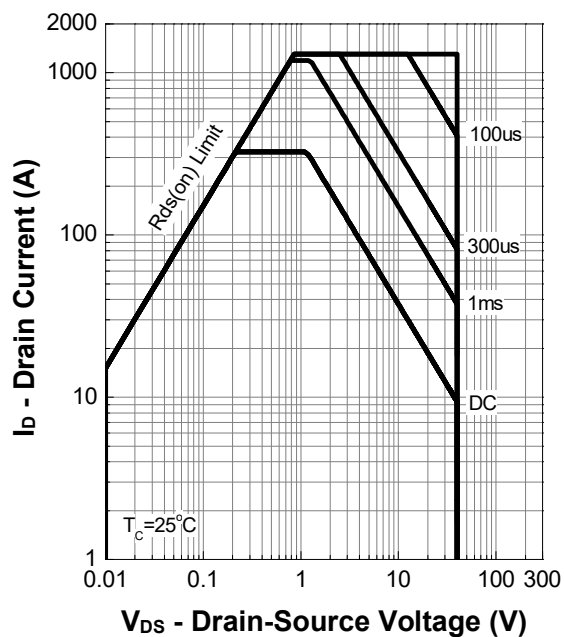
Power Capability



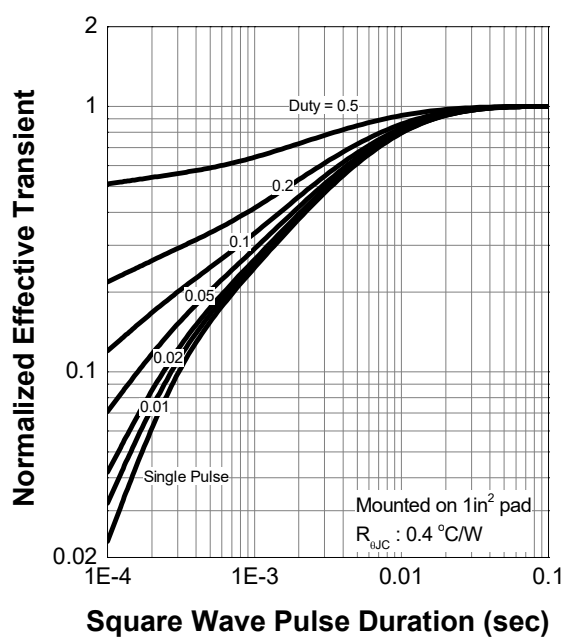
Current Capability



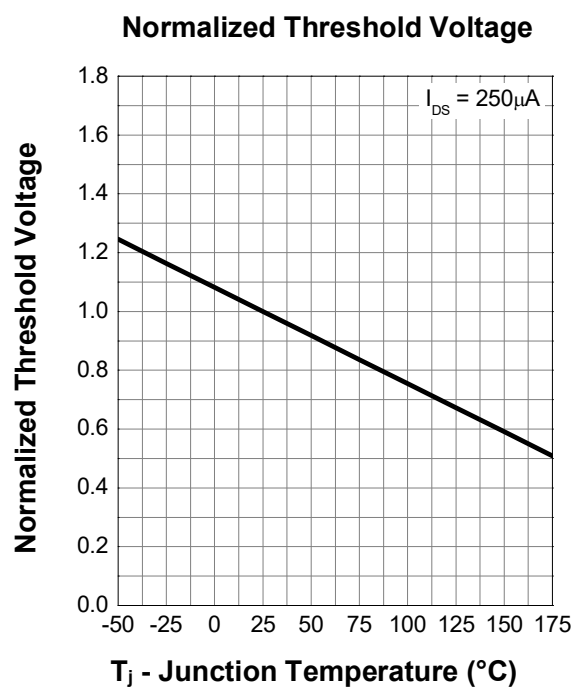
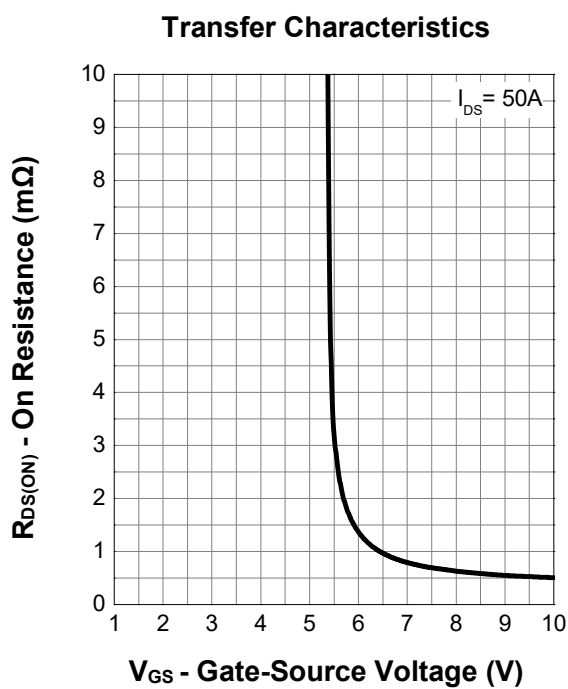
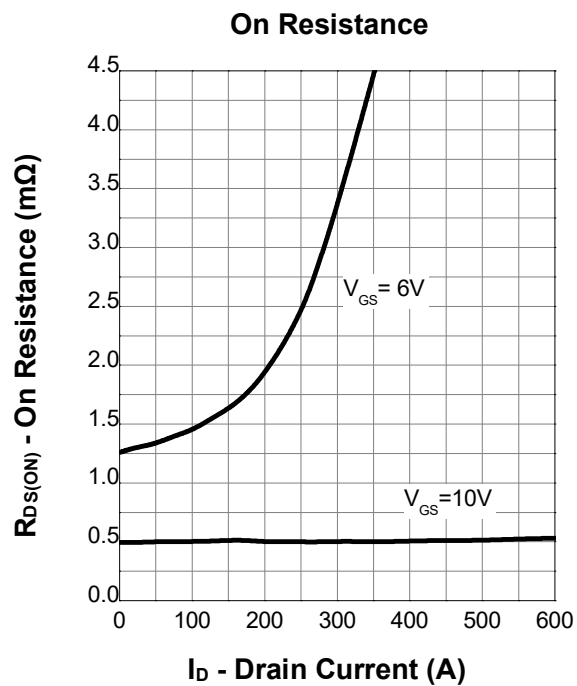
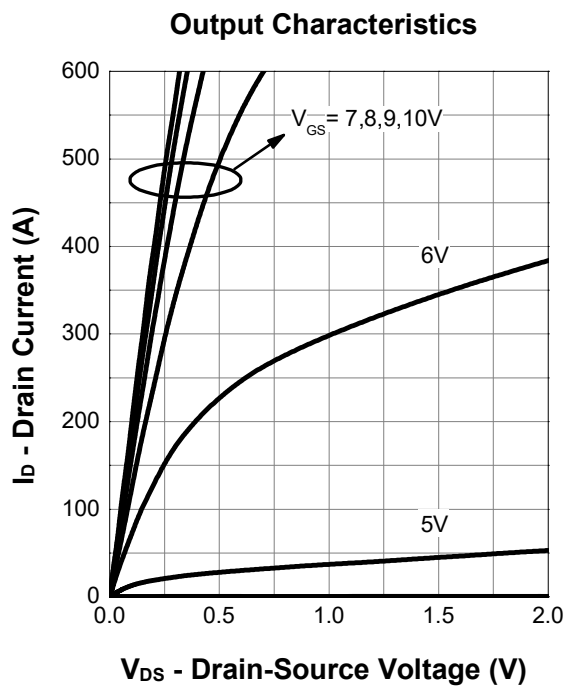
Safe Operation Area



Transient Thermal Impedance

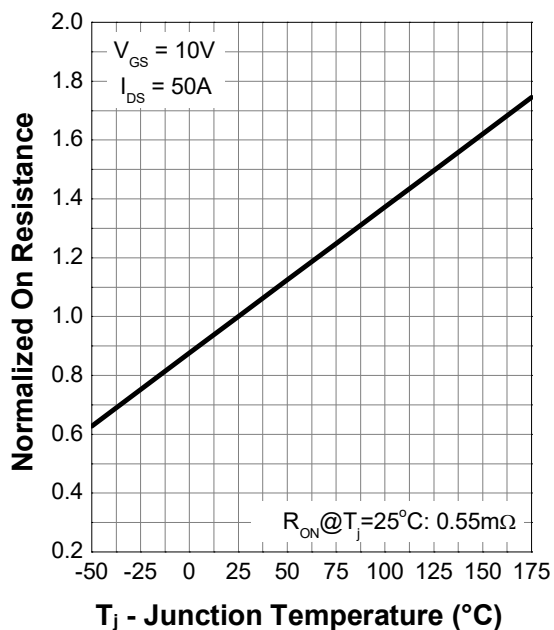


7. Typical Characteristics (Cont.)

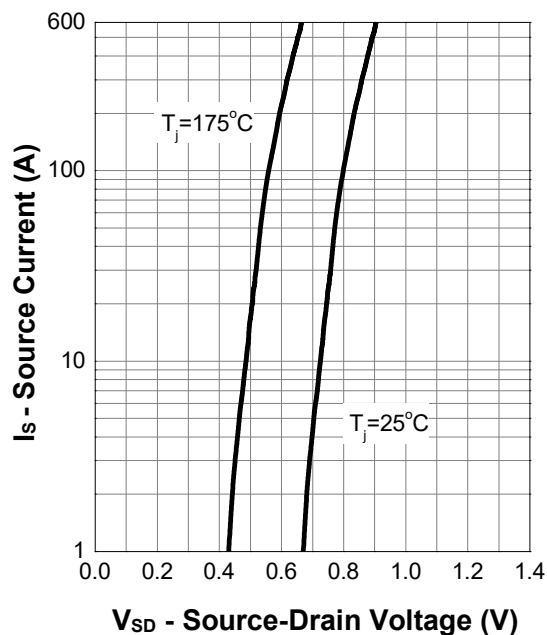


7. Typical Characteristics (Cont.)

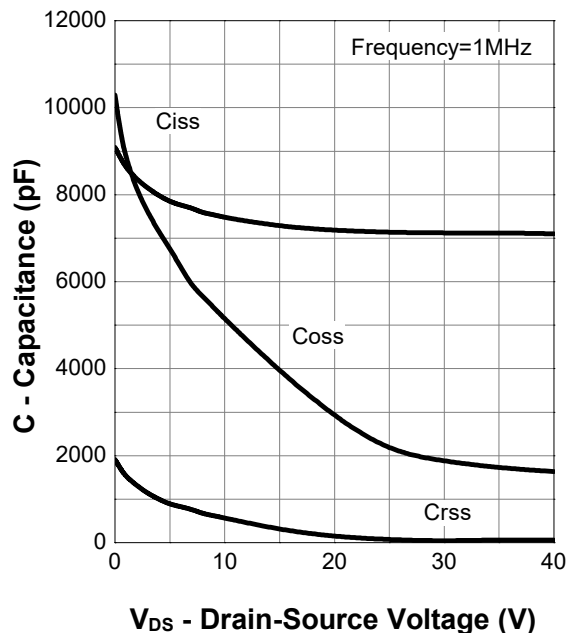
Normalized On Resistance



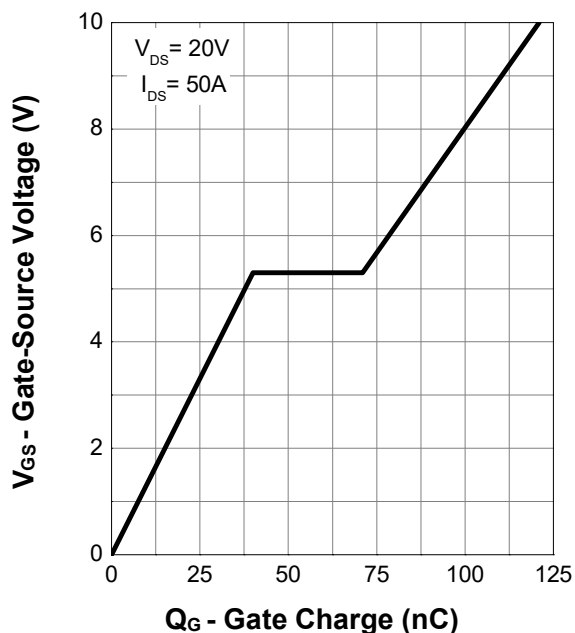
Diode Forward Current



Capacitance

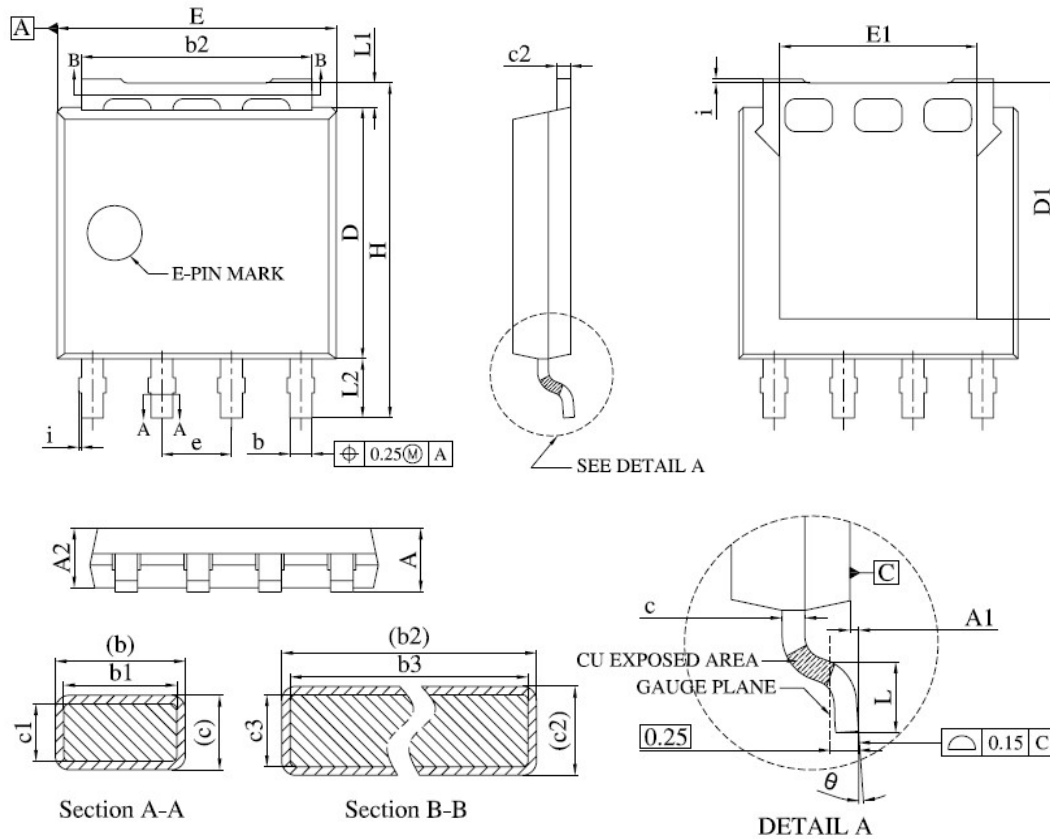


Gate Charge



8. Package Dimensions

LFPAK5*6 Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.00	1.30
A1	0.00	0.15
A2	0.98	1.12
b	0.35	0.50
b1	0.32	0.46
b2	4.02	4.41
b3	4.00	4.37
c	0.19	0.25
c1	0.17	0.23
c2	0.24	0.30
c3	0.22	0.28
D	4.45	4.70
D1	-	4.45
E	4.95	5.30
E1	3.50	3.70
e	1.27 BSC.	

Symbol	Dimensions In Millimeters	
	MIN.	MAX.
H	5.95	6.25
i	-	0.25
L	0.40	0.85
L1	0.27	0.57
L2	0.80	1.30
θ	0°	8°