

## N-Channel Enhancement Mode MOSFET

### 1. Product Information

#### 1.1 Features

- Surface-mounted package
- Low Thermal Resistance

#### 1.2 Applications

- Motor drivers
- DC - DC Converter

#### 1.3 Quick reference

- $BV \geq 40\text{ V}$
- $R_{DS(ON)} \leq 0.55\text{ m}\Omega @ V_{GS} = 10\text{ V}$
- $P_{tot} \leq 375\text{ W}$
- $R_{DS(ON)} \leq 1.65\text{ m}\Omega @ V_{GS} = 6\text{ V}$
- $I_D \leq 325\text{ A}$

### 2. Pin Description

Pin	Description	Simplified Outline	Symbol
1,2,3	Source	<p style="text-align: center;">Top View LFPAK5*6</p>	
4	Gate		
Tab	Drain		

### 3. Limiting Values

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>DS</sub>	Drain-Source Voltage	T <sub>C</sub> = 25 °C	-	40	V
V <sub>GS</sub>	Gate-Source Voltage	T <sub>C</sub> = 25 °C	-	± 20	V
I <sub>D</sub> <sup>*,***</sup>	Drain Current	T <sub>C</sub> = 25 °C, V <sub>GS</sub> = 10 V	-	325	A
		T <sub>C</sub> = 100 °C, V <sub>GS</sub> = 10 V	-	325	A
I <sub>DM</sub> <sup>*,**</sup>	Pulsed Source Current	T <sub>C</sub> = 25 °C, V <sub>GS</sub> = 10 V	-	1300	A
P <sub>tot</sub> <sup>*</sup>	Total Power Dissipation	T <sub>C</sub> = 25 °C	-	375	W
T <sub>stg</sub>	Storage Temperature		- 55	175	°C
T <sub>J</sub>	Junction Temperature		-	175	°C
I <sub>S</sub>	Diode Forward Current	T <sub>C</sub> = 25 °C	-	325	A
E <sub>AS</sub> <sup>*</sup>	Single Pulsed Avalanche Energy	V <sub>DD</sub> = 40 V , L = 1 mH	-	1152	mJ
R <sub>θJA</sub> <sup>*</sup>	Thermal Resistance- Junction to Ambient		42	-	°C / W
R <sub>θJC</sub> <sup>*</sup>	Thermal Resistance- Junction to Case		0.4	-	

Notes :

- \* Surface Mounted on 1 in<sup>2</sup> pad area, t ≤ 10 sec
- \*\* Pulse width ≤ 300 μs, duty cycle ≤ 1 %
- \*\*\* Limited by bonding wire

### 4. Marking Information

Product Name	Marking
UK005N04LFH	<div style="display: flex; align-items: center; gap: 10px;"> <div style="border: 1px solid black; padding: 2px;"> <b>005N04H</b>  <b>YWW01</b>  <b>AAAAAA</b> </div> <div> <b>YWW01:</b>                      Date Code                 </div> </div>

### 5. Ordering Code

Product Name	Package	Reel Size	Tape width	Quantity	Note
UK005N04LFH	LFPAK5*6			5000	

## 6. Electrical Characteristics (T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	40	-	-	V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>DS</sub> = 250 μA	2	-	4	V
I <sub>DSS</sub>	Zero Gate Voltage Source Current	V <sub>DS</sub> = 32, V <sub>GS</sub> = 0 V	-	-	1	μA
I <sub>GSS</sub>	Gate Leakage Current	V <sub>GS</sub> = ± 20 V, V <sub>DS</sub> = 0 V	-	-	± 100	nA
R <sub>DS(ON)</sub> <sup>a</sup>	Drain-Source On-State Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	-	0.50	0.55	mΩ
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 30 A	-	1.42	1.65	
<b>Diode Characteristics</b>						
V <sub>SD</sub> <sup>a</sup>	Diode Forward Voltage	I <sub>SD</sub> = 50 A, V <sub>GS</sub> = 0 V	-	-	1.3	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>SD</sub> = 45 A, dI <sub>SD</sub> /dt = 100 A/μs	-	76	-	nS
Q <sub>rr</sub>	Reverse Recovery Charge		-	54	-	nC
<b>Dynamic Characteristics<sup>b</sup></b>						
C <sub>iss</sub>	Input Capacitance	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 20 V Frequency = 1 MHz	-	7204	-	pF
C <sub>oss</sub>	Output Capacitance		-	2944	-	
C <sub>rss</sub>	Reverse Transfer Capacitance		-	147	-	
t <sub>d(on)</sub>	Turn-on Delay Time	V <sub>DS</sub> = 20 V, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 3.9 Ω, R <sub>L</sub> = 0.4 Ω, I <sub>DS</sub> = 50 A	-	26	-	nS
t <sub>r</sub>	Turn-on Rise Time		-	105	-	
t <sub>d(off)</sub>	Turn-off Delay Time		-	81	-	
t <sub>f</sub>	Turn-off Fall Time		-	72	-	
<b>Gate Charge Characteristics<sup>b</sup></b>						
Q <sub>g</sub>	Total Gate Charge	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 50 A	-	121	-	nC
Q <sub>gs</sub>	Gate-Source Charge		-	40	-	
Q <sub>gd</sub>	Gate-Drain Charge		-	31	-	

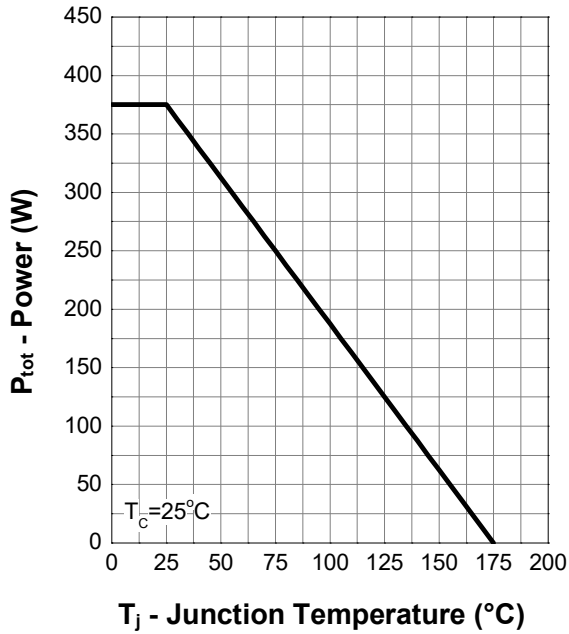
Notes :

a : Pulse test ; pulse width ≤ 300 μs, duty cycle ≤ 2 %

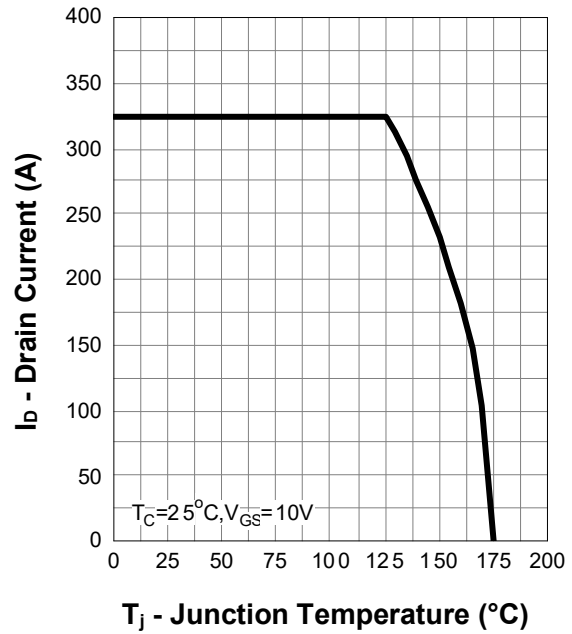
b : Guaranteed by design, not subject to production testing

### 7. Typical Characteristics (Cont.)

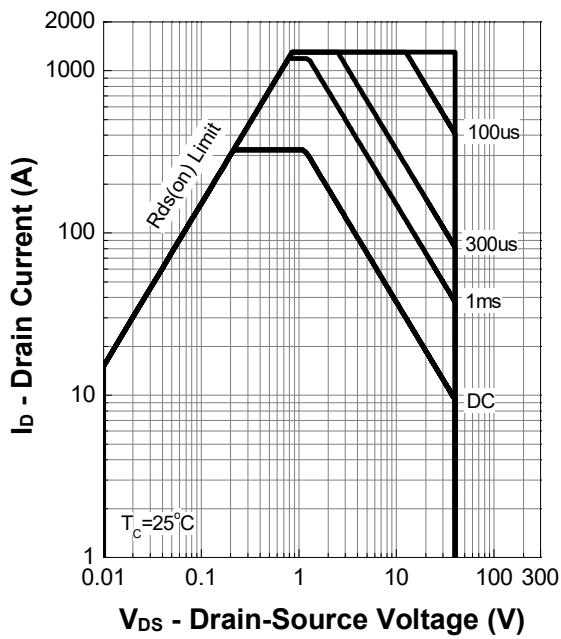
Power Capability



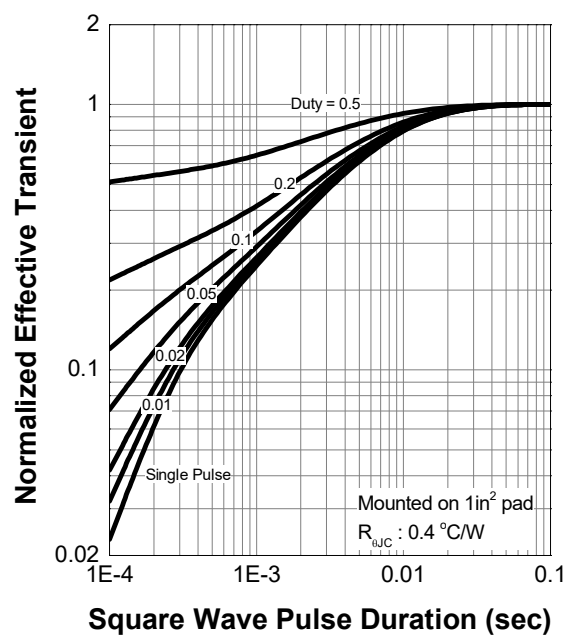
Current Capability



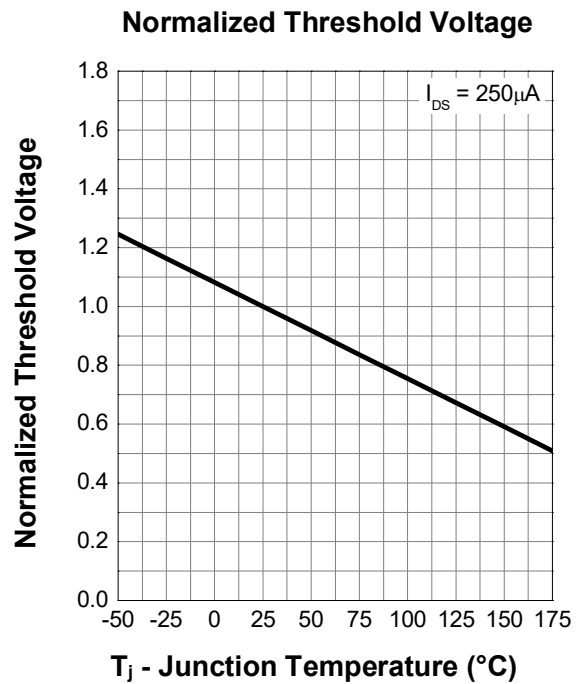
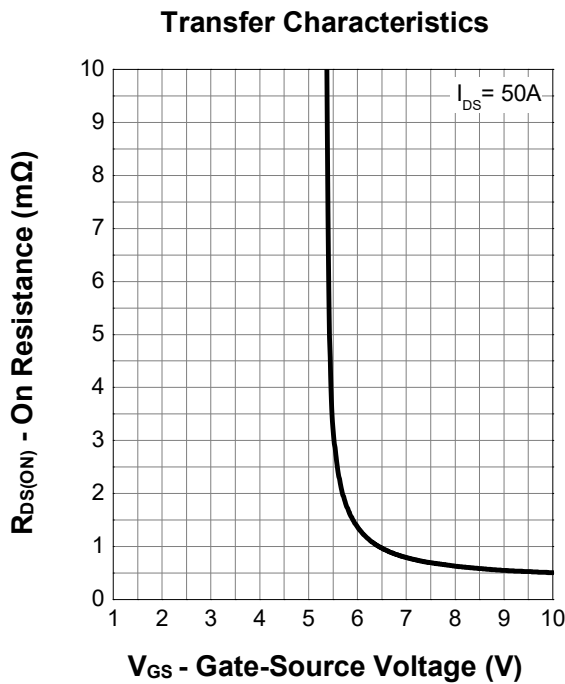
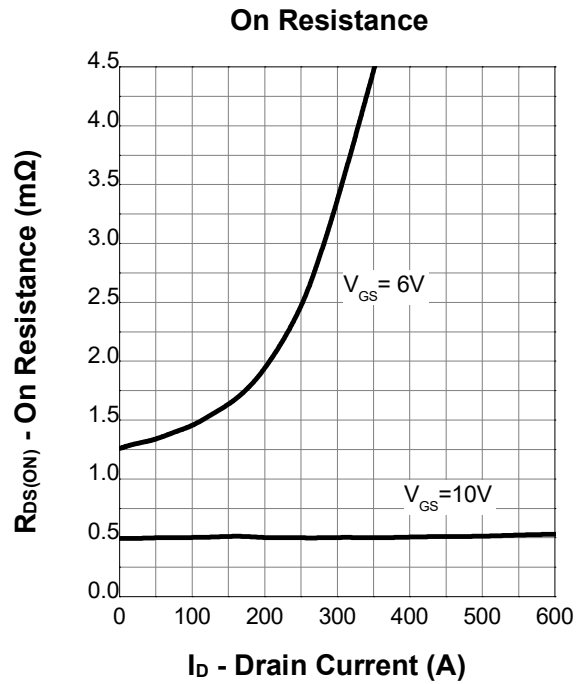
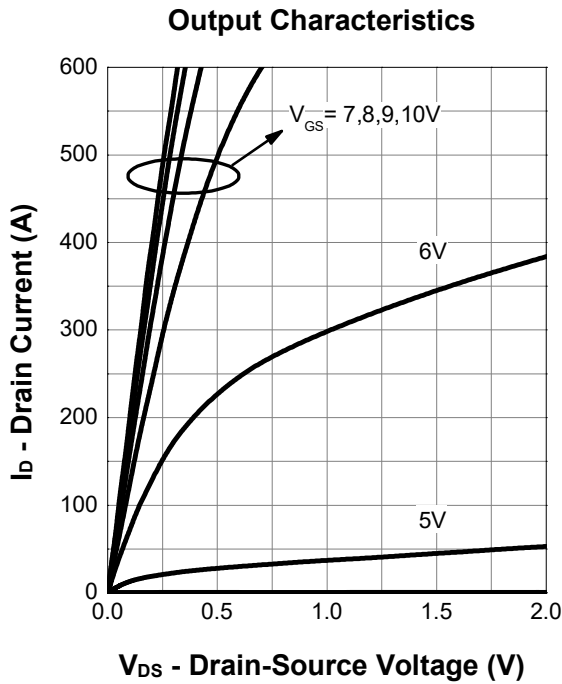
Safe Operation Area



Transient Thermal Impedance

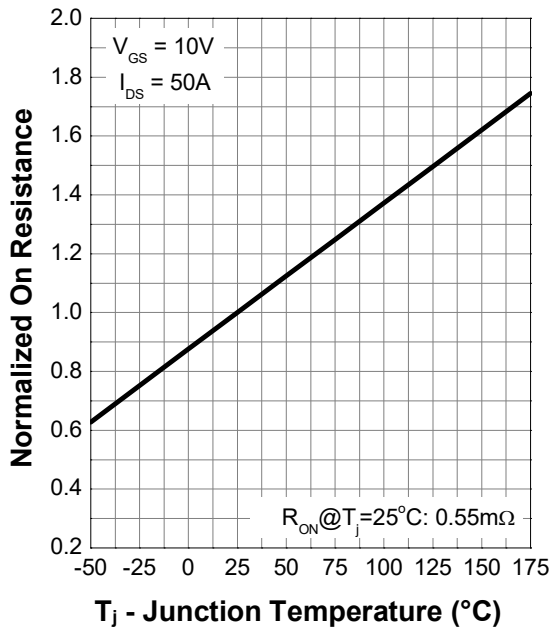


### 7. Typical Characteristics (Cont.)

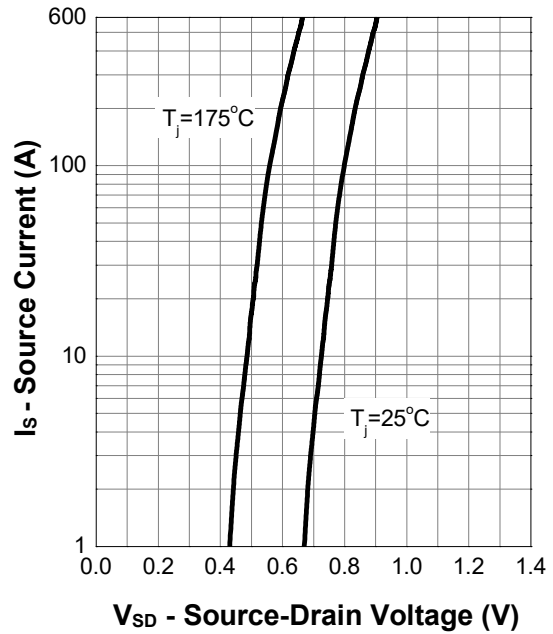


### 7. Typical Characteristics (Cont.)

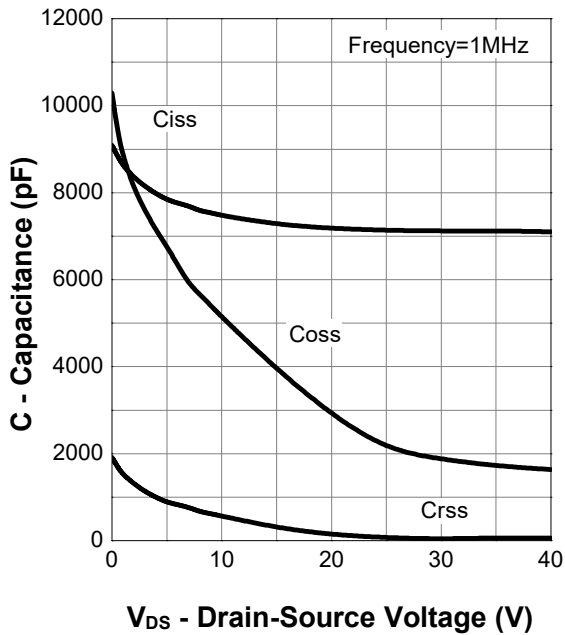
Normalized On Resistance



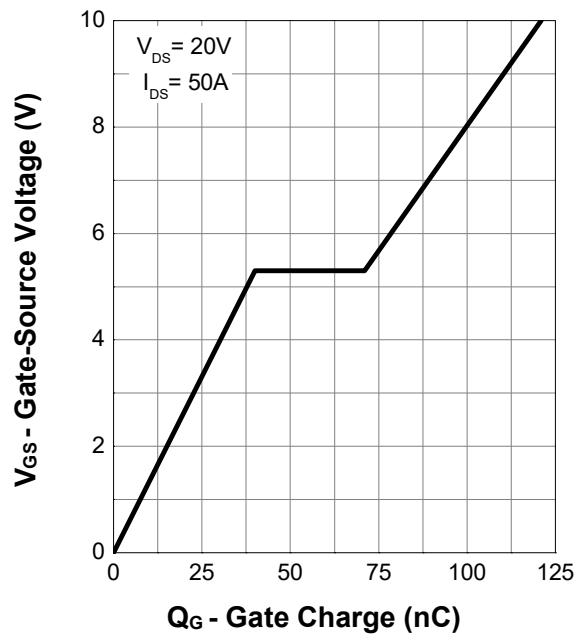
Diode Forward Current



Capacitance

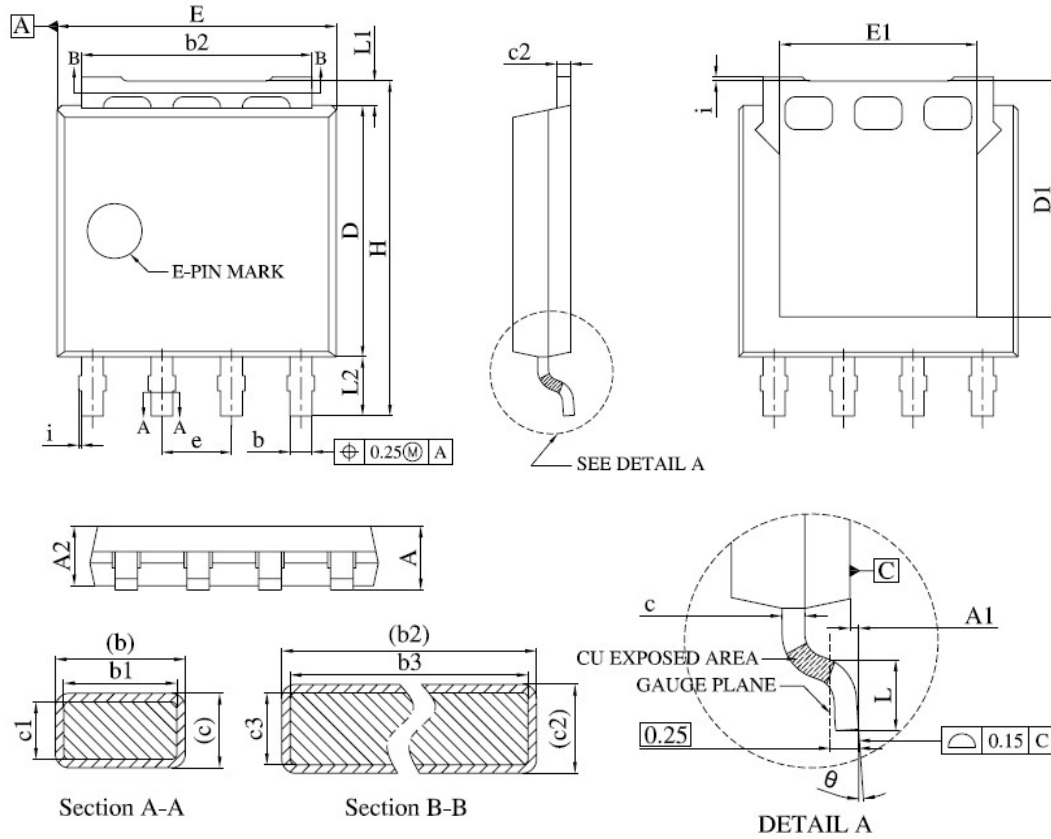


Gate Charge



## 8. Package Dimensions

### LFPAK5\*6 Package



Symbol	Dimensions In Millimeters	
	MIN.	MAX.
A	1.00	1.30
A1	0.00	0.15
A2	0.98	1.12
b	0.35	0.50
b1	0.32	0.46
b2	4.02	4.41
b3	4.00	4.37
c	0.19	0.25
c1	0.17	0.23
c2	0.24	0.30
c3	0.22	0.28
D	4.45	4.70
D1	-	4.45
E	4.95	5.30
E1	3.50	3.70
e	1.27 BSC.	

Symbol	Dimensions In Millimeters	
	MIN.	MAX.
H	5.95	6.25
i	-	0.25
L	0.40	0.85
L1	0.27	0.57
L2	0.80	1.30
θ	0°	8°